L Number	Hits	Search Text	DB	Time stamp
1	21077	ASIC	USPAT;	2004/06/08
	210,,		EPO; JPO; DERWENT	13:58
2	2001	ASIC near3 design\$3	USPAT; EPO; JPO; DERWENT	2004/06/08
3	938	ASIC adj3 design	USPAT; EPO; JPO;	2004/06/08 13:59
4	723	ASIC adj1 design	DERWENT USPAT; EPO; JPO;	2004/06/08 13:59
5	240	ASIC adj1 designs	DERWENT USPAT; EPO; JPO;	2004/06/08 13:59
6	370243	(volatile adj2 memory) or RAM or DRAM	DERWENT USPAT; EPO; JPO;	2004/06/08 14:01
7	3542	((volatile adj2 memory) or RAM or DRAM) near3 check\$3	DERWENT USPAT; EPO; JPO;	2004/06/08 14:01
8	872	(((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (processor or CPU)	DERWENT USPAT; EPO; JPO;	2004/06/08
9	405	(((volatile adj2 memory) or RAM or DRAM) near3 check\$3) near10 (processor or CPU)	DERWENT USPAT; EPO; JPO;	2004/06/08 14:02
10	297	(((volatile adj2 memory) or RAM or DRAM) near3 check\$3) near5 (processor or CPU)	DERWENT USPAT; EPO; JPO; DERWENT	2004/06/08 14:02
11	107	(((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (processor or CPU) same test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08 14:03
12	0	<pre>((((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (processor or CPU) same test\$3) and (ASIC near3 design\$3)</pre>	USPAT; EPO; JPO; DERWENT	2004/06/08 14:03
13	2		USPAT; EPO; JPO; DERWENT	2004/06/08 14:22
14	533	((((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same test\$3)	USPAT; EPO; JPO; DERWENT	2004/06/08 14:22
15	0	<pre>((((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (soft near2 test\$3))</pre>	USPAT; EPO; JPO; DERWENT	2004/06/08 14:23
16	0	<pre>((((volatile adj2 memory) or RAM or DRAM) near3 check\$3) same (soft near2 error\$1)) same test\$3</pre>	USPAT; EPO; JPO; DERWENT	2004/06/08
17	57	(((volatile adj2 memory) or RAM or DRAM) near3 checker)	USPAT; EPO; JPO;	2004/06/08 14:24
19	4	(((volatile adj2 memory) or RAM or DRAM) near3 checker) same self-test\$3	DERWENT USPAT; EPO; JPO;	2004/06/08 14:24
18	15	(((volatile adj2 memory) or RAM or DRAM) near3 checker) same test\$3	DERWENT USPAT; EPO; JPO;	2004/06/08 14:33
20	31	(((volatile adj2 memory) or RAM or DRAM) adj3 checker)	DERWENT USPAT; EPO; JPO;	2004/06/08 14:34
21	367	(((volatile adj2 memory) or RAM or DRAM) adj3 (checker or detector))	DERWENT USPAT; EPO; JPO; DERWENT	2004/06/08 14:38
22	200	(((volatile adj2 memory) or RAM or DRAM) adj2 (checker or detector))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:35
23	47	(((volatile adj2 memory) or RAM or DRAM) adj1 (checker or detector))	USPAT; EPO; JPO; DERWENT	2004/06/08 14:35

24	1 13			0004/06/00
24	13	(((volatile adj2 memory) or RAM or DRAM)	USPAT;	2004/06/08
		adj3 (checker or detector)) and ((test	EPO; JPO;	14:45
		adj2 data) or (test adj2 code\$1) or (test	DERWENT	
25	49	adj2 information))	Hanna.	2004/06/00
23	49	(((volatile adj2 memory) or RAM or DRAM) same (checker or detector)) same ((test	USPAT;	2004/06/08
		adj2 data) or (test adj2 code\$1) or (test	EPO; JPO;	14:44
	1	adj2 data) of (test adj2 code\$1) or (test adj2 information))	DERWENT	
26	7	(((volatile adj2 memory) or RAM or DRAM)	USPAT;	2004/06/08
20	1	same (checker or detector)) same ((test	EPO; JPO;	14:44
		adj2 data) or (test adj2 code\$1) or (test	DERWENT	14:44
		adj2 information)) same processor	DEKWENT	
29	106	detect\$3 adj3 (soft adj2 errors)	USPAT;	2004/06/08
23	100	detectys adjs (soft adjz effors)	EPO; JPO;	14:47
			DERWENT	14.47
30	147	detect\$3 adj3 (soft adj2 error\$1)	USPAT;	2004/06/08
50	17'	decector adjo (soit adjo ellolog)	EPO; JPO;	14:56
			DERWENT	14.56
31	13	(detect\$3 adj3 (soft adj2 error\$1)) same	USPAT;	2004/06/08
31	13	((volatile adj2 memory) or RAM or DRAM)	EPO; JPO;	14:48
		((Volatile adj2 memory) of RAM of DRAM)		14:40
32	2988	taylor-r\$.in.	DERWENT	2004/06/08
32	2 900	cayror-ry.in.	USPAT; EPO; JPO;	
			DERWENT	14:53
33	1	taylor-r\$.in. and ((((volatile adj2	USPAT;	2004/06/08
33		memory) or RAM or DRAM) adj3 (checker or		14:54
		detector)))	EPO; JPO; DERWENT	14:54
34	1	1	USPAT;	2004/06/08
34	1	memory) or RAM or DRAM) near3 check\$3)	EPO; JPO;	14:54
		same test\$3))	DERWENT	14:54
35	0	taylor-r\$.in. and (ASIC near3 design\$3)	USPAT;	2004/06/08
33		caylor 19:111: and (Abic hears designes)	EPO; JPO;	14:54
}			DERWENT	11.51
36	143	(error nearl detect\$3) near5 (soft adj2	USPAT;	2004/06/08
	1.5	error\$1)	EPO; JPO;	14:56
			DERWENT	14.30
37	159	(error\$1 near1 detect\$3) near5 (soft adj2	USPAT;	2004/06/08
• •		error\$1)	EPO; JPO;	14:56
			DERWENT	1
38	15	((error\$1 near1 detect\$3) near5 (soft	USPAT;	2004/06/08
		adj2 error\$1)) same ((volatile adj2	EPO; JPO;	15:01
	1	memory) or RAM or DRAM)	DERWENT	
39	159	((error\$1 near1 detect\$3) near5 (soft	USPAT;	2004/06/08
		adj2 error\$1))	EPO; JPO;	15:02
		-	DERWENT	
40	11	((error\$1 near1 detect\$3) near5 (soft	USPAT;	2004/06/08
		adj2 error\$1)) same test\$3	EPO; JPO;	15:41
			DERWENT	
41	207	DRAM near5 SRAM near5 processor	USPAT;	2004/06/08
			EPO; JPO;	15:43
			DERWENT	
42	2	(DRAM near5 SRAM near5 processor) same	USPAT;	2004/06/08
		printer	EPO; JPO;	15:44
		· ·	DERWENT	
43	17	(DRAM near5 SRAM near5 processor) and	USPAT;	2004/06/08
		printer	EPO; JPO;	15:44
			DERWENT	

L Number	Hits	Search Text	DB	Time stamp
1	0	"recovery adj2 module"	USPAT; EPO; JPO;	2004/06/08
2	474	recovery adj2 module	DERWENT USPAT;	2004/06/08
3	10	(recovery adj2 module) same self-test\$3	EPO; JPO; DERWENT USPAT;	16:42
4	17	/rocovoru nonn2 onnon) como cole bosto2	EPO; JPO; DERWENT	16:44
4	1,	(recovery near2 error) same self-test\$3	USPAT; EPO; JPO; DERWENT	2004/06/08
5	204	(recovery near2 error) and self-test\$3 and ram	USPAT; EPO; JPO; DERWENT	2004/06/08 16:46
6	195	(recovery near2 error) and self-test\$3 and ram and processor	USPAT; EPO; JPO;	2004/06/08
7	1	(recovery near2 error) and self-test\$3 and ram and processor and	DERWENT USPAT; EPO; JPO;	2004/06/08 16:47
8	177	re-initialization (recovery near2 error) and self-test\$3 and ram and processor and initialization	DERWENT USPAT; EPO; JPO;	2004/06/08 16:47
9	1	(recovery near2 error) and self-test\$3 and ram and processor and	DERWENT USPAT; EPO; JPO;	2004/06/08 16:50
10	8056	reinitialization "printer controller"	DERWENT USPAT; EPO; JPO;	2004/06/08
11	0	"printer controller" and ((recovery near2 error) and self-test\$3 and ram and	DERWENT USPAT; EPO; JPO;	2004/06/08
12	1	<pre>processor and initialization) "printer controller" and (recovery adj2 module)</pre>	DERWENT USPAT; EPO; JPO;	2004/06/08
13	33	"self-test" same CRC	DERWENT USPAT;	2004/06/08
14	1247	(self-test\$3 or (self adj2 test\$3)) near3	EPO; JPO; DERWENT USPAT;	2004/06/08
15	802	<pre>(RAM or DRAM or memory) (self-test\$3 or (self adj2 test\$3)) near2</pre>	EPO; JPO; DERWENT USPAT;	2004/06/08
1.0		(RAM or DRAM or memory)	EPO; JPO; DERWENT	16:57
16	195	<pre>(self-test\$3 or (self adj2 test\$3)) near2 (RAM or DRAM)</pre>	USPAT; EPO; JPO; DERWENT	2004/06/08
17	144	<pre>(self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)</pre>	USPAT; EPO; JPO; DERWENT	2004/06/08 17:01
18	2	((self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)) same recovery	USPAT; EPO; JPO;	2004/06/08 16:59
19	16	((self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)) and recovery	DERWENT USPAT; EPO; JPO;	2004/06/08 17:01
20	19	((self-test\$3 or (self adj2 test\$3)) adj2 (RAM or DRAM)) same processor	DERWENT USPAT; EPO; JPO; DERWENT	2004/06/08 17:01